

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional)

14993.13

Application Number 10/615,440

Not Yet Assigned

Applicant(s)

Joseph Walsh et al.

Filing Date

July 7, 2003

Group Art Unit

2817

Not Yet Assigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
3	1	6,181,169	1/30/2001	Hu	327	77	10/28/1999
3	2	5,789,949	8/4/1998	Giordano et al.	327	63	9/25/1996
3	3	4,859,961	8/22/1989	Davenport	330	253	12/19/1998
3	4	4,859,872	8/22/1989	Hyakutake	307	269	3/23/1988
3	5	4,825,100	4/25/1989	Caspell	307	279	4/4/1988
3	6	4,585,961	4/29/1986	Daubert	307	491	1/19/1984
3	7	5,142,244	8/25/1992	Glica et al.	330	253	5/6/1991
3	8	6,124,759	9/26/2000	Migliavacca	330	253	4/9/1998

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

3	9	CMOS Analog Circuit Design; Phillip E. Allen & Douglas R. Holberg 1987 Copyright Holt, Rinehart and Winston p 356 & 410					

EXAMINER

S J Mottola

DATE CONSIDERED

8-4-04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.